

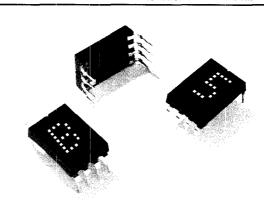
NUMERIC and HEXADECIMAL INDICATORS

5082-7300 **SERIES**

TECHNICAL DATA APRIL 1976

Features

- 0-9, Test State, Minus Sign, Blank States **Decimal Point** 7300 Right Hand D.P. 7302 Left Hand D.P.
- NUMERIC 5082-7300/-7302
 HEXADECIMAL 5082-7340 0-9, A-F, Base 16 Operation Blanking Control, Conserves Power No Decimal Point
- DTL/TTL COMPATIBLE
- INCLUDES DECODER/DRIVER WITH 5 BIT MEMORY
- 8421 Positive Logic Input • 4 x 7 DOT MATRIX ARRAY
- Shaped Character, Excellent Readibility
- STANDARD .600 INCH x .400 INCH DUAL-IN-LINE PACKAGE INCLUDING CONTRAST FILTER
- **CATEGORIZED FOR LUMINOUS INTENSITY** Assures Uniformity of Light Output from Unit to Unit within a Single Category



Description

The HP 5082-7300 series solid state numeric and hexadecimal indicators with on-board decoder/driver and memory provide a reliable, low-cost method for displaying digital information.

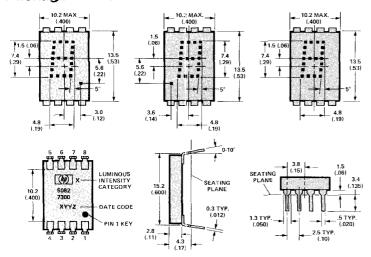
The 5082-7300 numeric indicator decodes positive 8421 BCD logic inputs into characters 0-9, a "-" sign, a test pattern, and four blanks in the invalid BCD states, The unit employs a right-hand decimal point. Typical applications include point-of-sale terminals, instrumentation, and computer systems.

The 5082-7302 is the same as the 5082-7300, except that the decimal point is located on the left-hand side of the

The 5082-7340 hexadecimal indicator decodes positive 8421 logic inputs into 16 states, 0-9 and A-F. In place of the decimal point an input is provided for blanking the display (all LED's off), without losing the contents of the memory. Applications include terminals and computer systems using the base-16 character set.

The 5082-7304 is a (± 1.) overrange character, including decimal point, used in instrumentation applications.

Package Dimensions



PIN	FUN	FUNCTION					
	5082-7300 and 7302 Numeric	5082-7340 Hexadecima					
1	Input 2	Input 2					
2	Input 4	Input 4					
3	Input 8	Input 8					
4	Decimal point	Blanking control					
5	Latch enable	Latch enable					
6	Ground	Ground					
7	V _{cc}	V _{cc}					
8	Input 1	Input 1					

NOTES:

- 1. Dimensions in millimeters and (inches).
- 2. Unless otherwise specified, the tolerance is ±.38mm (±.015").
- 3. Vertical digit center line is ±.51mm (±.02") from vertical package center line.

ABSOLUTE MAXIMUM RATINGS

DESCRIPTION	SYMBOL	MIN	MAX	UNIT
Storage temperature, ambient	T _S	-40	+100	°c
Operating temperature, case	Τ _C	-20	+85	°C
V _{CC} Pin potential to ground pin	V _{cc}	-0.5	+7.0	V
Voltage applied to input logic pins and decimal point [1]	V_{i}	-0.5	+5.5	ν
Voltage applied to latch enable	T VE	-0.5	+5.5	7 V
Voltage applied to blanking control [2]	V _B	-0.5	+5.5	V

NOTES: 1. Decimal point applies only to 7300/7302. 2. Applies only to 7340.

RECOMMENDED OPERATING CONDITIONS

DESCRIPTION	SYMBOL	MIN	NOM	MAX	UNIT
Supply Voltage	V _{cc}	4,5	5.0	5.5	V
Low Level Input Voltage	VIL	0		0.8	٧
High Level Input Voltage	ViH	2.0		5.25	ν
Latch enable voltage-data being entered	VEL	0		0.8	P4V:54
Latch enable voltage-data not being entered	VEH	2.0		5.25	V
Blanking control voltage-display not blanked [1]	V _{BL}	0		0.8	V
Blanking control voltage-display blanked [1]	V _{BH}	3.5		5.25	٧

NOTE: 1. Applies only to 7340.

ELECTRICAL/OPTICAL CHARACTERISTICS (T_C = -20°C to +85°C, unless otherwise specified)

DESCRIPTION	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Supply current	lcc	V _{CC} = 5.5V		94[1]	170[2]	mA
Power dissipation	P _T	V _{CC} = 5.5V		470[1]	935[2]	mW
Luminous intensity per LED (Digit average) [3] [5]	l _v	V _{CC} = 5.0V, T _C = 25°C	32	70		μcd
Enable Pulse Width	tw	V _{CC} = 5.0V, V _{EL} = 0.4V	120			nsec
Time data must be held before positive transition of enable line	tsetup	V _{IL} = 0.4V, V _{EH} = 2.4V	50			nsec
Time data must be held after positive transition of enable line	tHOLD	$V_{IH} = 2.4V, T_C = 25^{\circ}C$	50			nsec
Blanking control current "L" state[4]	I _{BL}	V _{CC} = 5.5V, V _{BL} = 0.8V			200	μΑ
Blanking control current "H" state [4]	I _{BH}	V _{CC} = 5.5V, V _{BH} = 4.5V		Normali	2.0	mA
Logic and latch enable currents "L" state	lie Jeu	V _{CC} = 5.5V V _I , V _E = 0.4V			-1,6	mA
Logic and latch enable currents "H" state	I _{IH} I _{EH}	V _{CC} = 5.5V V _I , V _E = 2.4V			+250	μА
Peak wavelength	λ _{PEAK}	T _C = 25°C		655		nm
Spectral halfwidth	Δλ 1/2	T _C = 25°C		30		nm
Weight				0.8		gm

NOTES: 1. VCC = 5.0V with statistical average number of LED's lit. 2. Worst case condition excluding test state on 5082-7300/-7302.

^{3.} The digits are categorized for luminous intensity such that the variation from digit to digit within a category is not discernible to the eye. Intensity categories are designated by a letter located on the reverse side of the package contiguous with the Hewlett-Packard logo marking.

^{4.} Applies only to 7340.

^{5.} I_V as a function of temperature, I_V(T), may be calculated from the relationship; I_V(T) = I_V(25°C) (.985) [TC ~ 25°C]; I_V(T) = Luminous intensity at any particular case temperature; I_V(25°C) = Luminous intensity at T_C = 25°C; T_C = Case temperature at which luminous intensity is to be calculated.

TRUTH TABLE FOR 5082-7300 SERIES DEVICES

CHARA	CTER			INPL	JTS			
5082- 7300/7302 Numeric	5082 7340 Hex.	Х8	X4	X2	ХI	Е	B[1]	73 NL
0	0	- 6	L	ı	L	L	L	1
		L	L	L	Н	L		E
2	2		L	Н	L	L	L	E
3	3	L	L	H	Н	L	L	A
4	4	L	Н	L	L	L	L	
5	5	E	H	L	Н	L	L	E
6	6	L	H	н	L	L	L	F
7	7.	L	Н	Н	Н	L	L	
8	8	H	L	L	L	L	L	P
9	9	H	L	L	Н	L	L	E p

CHARA	CTER			INPL	JTS		
5082- 7300/7302 NUMERIC	5082 7340 Hex.	Х8	Х4	ж2	Χí	Ε	B[1]
Test	A	н	L	н	L	L	L
Blank	8	14	L	Н	н	L	L
Blank	С	H	Н	L	L	L	L
Minus	D	14	Н	L	Н	L	L
Blank	E	H	Н	Н	L	L	
Blank	F	H	Н	Н	Н	L	L
Hold	Hold	Х	Х	×	Х	Н	X
-	Blank [1]	Х	X	Х	X	Х	Н
Decimal pt. on [2]	4			DP ₁ =	L		
Decimal pt. off [2]	_	DP _i = H					

NOTES:

- 1. The blanking control input, B, pertains to the 5082-7340 Hexadecimal indicator only.
- 2. The decimal point input pertains to the 5082-7300 and -7302 Numeric Indicators only.
- 3. H = logic 'High'; L = logic 'Low'; x = 'don': care'.

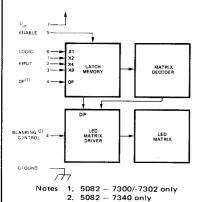


Figure 1. Block Diagram of 5082-7300 Series Logic.

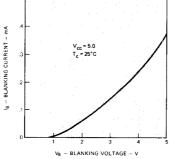


Figure 2. Typical Blanking Control Current Vs. Voltage for 5082-7340 Only.

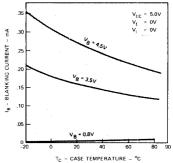


Figure 3. Typical Blanking Control Input Current Vs. Temperature, 5082-7340.

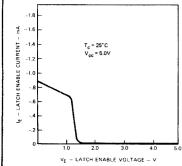


Figure 4. Typical Latch Enable Input Current Vs. Voltage for the 5082-7300 Series Devices.

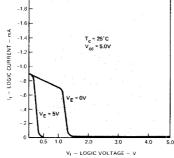


Figure 5. Typical Logic and Decimal Point Input Current Vs. Voltage for the 5082-7300 Series Devices. Decimal Point Applies to 5082-7300 Only.

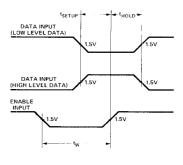
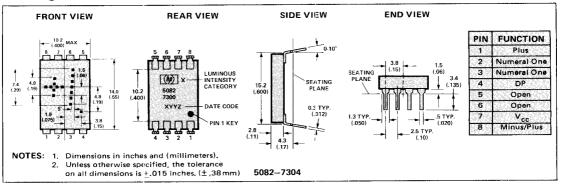


Figure 6. Timing Diagram of 5082-7300 Series Logic.

SOLID STATE OVER RANGE CHARACTER

For display applications requiring a \pm , 1, or decimal point designation, the 5082-7304 over range character is available. This display module comes in the same package as the 5082-7300 series numeric indicator and is completely compatible with it.

Package Dimensions



TRUTH TABLE FOR 5082-7304

CHARACTER		Plf		
	1	2,3	4	8
	4.2	×	X	H
		X	X	H
	X	Н	X	×
Decimal Point	X	X	Н	X
Blank		L	1	

NOTES: L: Line switching transistor in Fig. 7 cutoff.

H: Line switching transistor in Fig. 7 saturated.

X: 'don't care'

Absolute Maximum Ratings

DESCRIPTION	SY	MBOL	MIN	MAX	UNIT
Storage temperature, ambient		т,	-40	+100	°c
Operating temperature, case		T _C	-20	+85	°C
Forward current, each LED		T _E		10	mA
Reverse voltage, each LED		٧A		4	ν

RECOMMENDED OPERATING CONDITIONS

	SYMBOL	MIN	NOM	MAX	UNIT
LED supply voltage	Vcc	4.5	5.0	5.5	V
Forward current, each LED	JF.	Wi	5.0	10	mA.

NOTE:

LED current must be externally lim ted. Refer to figure 7 for recommended resistor values.

Electrical/Optical Characteristics (T_C = -20°C TO +85°C, UNLESS OTHERWISE SPECIFIED)

DESCRIPTION	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Forward Voitage per LED	V _E	I _E = 10 mA		1.6	2.0	٧
Power dissipation	P _T	I _F = 10 mA all diodes lit		250	320	mW
Luminous Intensity per LED (digit average)	I ₀	I _F = 6 mA T _C = 25°C	32	70		μcd
Peak wavelength	λpeak	T _C = 25°C		655		nm
Spectral halfwidth	Δλ1/2	T _C = 25°C		30		nm
Weight	P. Barkery			8.0		gm